#5 6/6/02 Mucish PATENT

29926/37833

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Hyung-Bok Choi

Serial No.: To be assigned

Filed: January 21, 2002

(herewith)

For: Semiconductor Device and Method of Fabricating the Same

I hereby certify that this paper and the documents referred to as enclosed therewith are being deposited with the United States Postal Service on January 22, 2002, in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231 utilizing the "Express Mail Post Office to Addressee" service of the United States Postal Service under Mailing Label No. EV027096838US

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**INFORMATION DISCLOSURE STATEMENT** 

Commissioner for Patents Washington, D.C. 20231

Sir:

Submitted herewith for consideration by the examiner are copies of the documents identified on the attached Form PTO-1449.

Entry and consideration of the submitted documents is solicited.

Respectfully submitted,

MARSHALL, GERSTEIN & BORUN 6300 Sears Tower 233 South Wacker Drive Chicago, Illinois 60606-6357

(312) 474-6650

January 22, 2002

By:

Reg. No. 25,902